$2.5B+

Valuation via $175M Series F Funding

"The market has spoken and made it abundantly clear that RISC-V computing will be competing for the heart of all future computing platforms. As the founder and market leader of RISC-V computing it’s our role to lead this ecosystem forward and offer customers advanced computing alternative to Arm and others. This valuation is a validation of our strategy, our incredible team, and our singular focus on building the leading portfolio of high-performance RISC-V compute products in the market. Our customers are signaling strong demand for SiFive to deliver the highest level of performance as quickly as possible."

- Patrick Little, President, Chairman, and CEO, SiFive
“As the leading open-source ISA, RISC-V offers a level of scalability and customization that is unique in the industry. There is strong demand from foundry customers to support more RISC-V IP offerings. As part of the new innovation fund, Intel is planning investments and offerings that will strengthen the ecosystem and help drive further adoption of RISC-V. The fund will help disruptive RISC-V companies innovate faster through IFS by collaborating on technology co-optimization, prioritizing wafer shuttles, supporting customer designs, building development boards and software infrastructure and more.” – Intel PR
The Top 3 Global ISAs

x86  RISC-V®  arm
The Founder and Leader in RISC-V IP

MISSION
- Create the best computing platforms in the world
- Enable our customers to deliver differentiated, best-in-class products
- Be the clear leader in RISC-V Processor IP

Customers
- Over 300 Design Wins
- Over 100 Customers
- 8/10 Top Semiconductor Co.
- Partnered w/Leading Foundries

Technology
- Fastest Commercially Available RISC-V IP
- Industry-Leading RISC-V Portfolio
- Develop Hardware in a Software Fashion

RISC-V Expertise
- Home to the Inventors of RISC-V
- Global Presence
- 400 Employees & Hiring!
  - 350+ Engineers
- Key Roles at RISC-V International
- Founded in 2015
- $350M+ Total Raised
  - 5x Valuation Increase (Series E - F)
  - 4x YoY Revenue Growth (‘20 – ‘21)
“SiFive’s leadership in RISC-V positions them well to capture fast-growing performance-driven markets including automotive, client computing, data center, and mobile. These areas increasingly require AI/ML enabled solutions to deliver increased efficiency and better experiences, aligning to the open nature of RISC-V. More and more workload-focused computing strategies are quickly coming to market, creating opportunities for SiFive’s performance-driven roadmap to enable.”

- Stefan Dykerhoff, Sutter Hill Ventures

“The advancement of RISC-V enables new designs to bring compute closer to memory and become more efficient. SiFive’s portfolio of RISC-V processor IP is competitive with the market leaders at several performance levels, and this new investment accelerates the company to compete at higher levels.”

- Yongtak Song, Venture Investment, SK hynix
Investor Support

“The rapid rise of SiFive into the performance processor IP market is very exciting, and we’re pleased to support their mission to deliver world-class computing platforms into the sizeable and growing market for workload-focused chip design. The leadership team at SiFive are unlocking the potential of RISC-V at an incredible rate, expanding their leading portfolio to create competition for legacy IP companies as chip designers ask for the building blocks they need to build new designs for their specific applications. We see huge potential for future growth and exciting new products in many markets, including automotive, client computing platforms, data center, the intelligent edge and more.

- Mike Donnelly, Partner, IBEX Investors

“The ability to drive and grow the RISC-V ecosystem will be critical to industries, especially IoT, automotive and embedded compute systems, that are looking for more advanced open source hardware. This entrepreneurial spirit from partners like SiFive is what makes the Samsung Advanced Foundry Ecosystem (SAFE) the foundation for enabling our customers to innovate and design the next generation SoC.”

- Moonsoo Kang, EVP & Head of Foundry Business Development Team, Samsung Electronics
“It will take a determined company like SiFive to make significant inroads into the myriad processing needs currently served by various microprocessor architectures. SiFive continues to innovate across multiple performance tiers, taking the RISC-V architecture where it needs to go if it’s to gain the sort of traction in the industry that its worldwide network of advocates seeks.

“With this latest funding round coupled with its share of the recently announced billion-dollar Intel Foundry Services Investment Fund, SiFive appears to be determined to accumulate a stockpile of cash that will allow the company to accelerate its already torrid pace in developing new RISC-V processor cores up and down the price, performance, and power spectrum.”

- Steve Leibson, Principal Analyst, TIRIAS Research

“The Series F raise of $175m by SiFive and the news earlier in the week of the divestiture of the OpenFive assets to Alphawave for $210m, represents strategic signaling of intent to the market by SiFive. Against a dynamic market landscape SiFive is demonstrating its commitment and focus to competing with some of the industry's largest competitors, bolstered by the support of the burgeoning RISC-V ecosystem.”

- Steven Dickens, Senior Analyst, Futurum Research
“IFS believes in enabling a multi-ISA strategy, including RISC-V as the open compute base for future platforms,” said.
“Our IFS investment in RISC-V includes partnering with SiFive for building the Horse Creek developer platform that will
be broadly available in late 2022, based on Intel 4 process technology.”

“We would like to congratulate our partner SiFive and are excited to see the next level of investment in SiFive as we
work together to develop high-performance RISC-V based automotive solutions. Our expertise in the automotive
field combined with SiFive’s high-performance RISC-V products will deliver innovative, next-generation solutions to
our automotive customers worldwide.”
- Takeshi Kataoka, SVP, GM, Automotive Solution BU, Renesas

“Microchip has revolutionized the FPGA industry with the highly awarded mid-range FPGA and SoC portfolio – the
PolarFire® FPGA family, and the first RISC-V based PolarFire SoC, delivering best-in-class power efficiency, reliability
and security. With this portfolio we have enabled innovation in commercial applications like AI-enabled embedded
vision and smart automation products, secure and reliable 5G communications networks, as well as sophisticated
aerospace and defense products. Our partnership with SiFive has been vital in our ability to offer these innovative
programmable solutions as well as bring highly-differentiated RISC-V processing solutions to the market.”
- Bruce Weyer, VP, FPGA Business Unit, Microchip
Recent Announcements

- Introduced new Product family branding aligned to Market Focus
- Entered Performance-driven markets with the fastest commercially available RISC-V IP
- Entered AI & ML-enabled markets with 2 RISC-V vector enabled processor IP products
- Execution of 3 Performance product launches & 3 RISC-V portfolio feature in 1 year
- Intel Foundry Services IP-Alliance Partner with optimized RISC-V IP & Next-Gen Developer Board

Design Wins
- Renesas
- Tenstorrent
- Deep Vision
- ArchiTek AiOnic
- eTopus
- FADU
- Synaptics
- Intel Foundry Services
- Samsung
- Qualcomm
- & Other Top Semiconductor Co’s

Partnerships
- Intel Foundry Services
  - IP Optimized for Intel 4 Process Technology
  - “Horse Creek” RISC-V Developer Platform in 2022
- Samsung Foundry
  - AI SoC Development Platform
- TSMC
  - First N5 RISC-V SoC Tapeout
- DARPA
  - First RISC-V IP provider
- Canonical
  - Ubuntu OS
- Green Hills
  - INTEGRITY RTOS
- SEGGER
  - emRun library
- SYSGO
  - PikeOS RTOS
- Wind River
  - VxWorks RTOS
- DARPA
  - First RISC-V IP provider
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- SYSGO
  - PikeOS RTOS
- Wind River
  - VxWorks RTOS

RISC-V Ecosystem

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RISC-V Portfolio

An Industry Leading Commercial Portfolio of RISC-V processor IP
- Enabling Workload-focused SoC design from embedded to performance-driven, AI/ML, and HPC markets
- Enabling choice in the Semiconductor performance processor IP market
- Designed by the Inventors of RISC-V & their expert teams

SiFive Performance™
- Designed for maximum throughput while preserving efficiency
- Multi-core & multi-cluster capabilities
- Hypervisor & Virtualization enabled
- Lower area vs. competitor cores of same performance

SiFive Intelligence™
- High-performance application processor with scalable vector compute
- Unified Vector & Scalar Compute Capabilities
- Optimized TensorFlow Lite Support
- Multi-core & multi-cluster capabilities

SiFive Essential™
- Family of processor cores for embedded and small-application uses
- Scalable & configurable designs
- SiFive Insight™ advanced trace & debug
- SiFive Shield™ whole SoC security
- Mix+Match capability for heterogeneous SoC design
SiFive has entered the Application Processor Market with many licensed uses:

- Automotive, Client, Data Center, Edge, & Hyperscale markets
- Multiple SiFive Intelligence™ X280 licenses signed since Q2 2021 launch, including:
  - Tenstorrent
  - Deep Vision
- $1B Intel Innovation Fund to create foundry ecosystem
  - SiFive IP optimized for latest IFS process
- Multiple SiFive Performance™ P550 licenses signed since Q3 2021 launch
  - Intel “Horse Creek” multi-core SoC taped-out Q1 2022
    - Developer board design underway to further enable RISC-V developers
- Strong Industry Interest in SiFive Performance P650 after introductory announcement in Q4 2021
The Future of RISC-V Has No Limits

The Industry believes in RISC-V
- We’ve made huge progress in the past 6 years
- RISC-V is Inevitable

2021 was an inflection point for RISC-V
- RISC-V has grown far beyond embedded apps
- Partners and ecosystem have arrived

Imagine the next 2–3 years
- RISC-V in every market & compute point
- Every tech company has a RISC-V Strategy
Thank You
Announcement Links:

Innovation Starts Here: Partnering to Power the Foundry Ecosystem – Opinion by Randir Thakur, President, Intel Foundry Services

Intel Launches $1 Billion Fund to Build a Foundry Innovation Ecosystem – Intel Press Release

Intel Foundry Services Launches Ecosystem Alliance to Accelerate Customer Innovation – Intel Press Release

Intel Corporation Makes Deep Investment in RISC-V Community to Accelerate Innovation in Open Computing – RISC-V International Press Release

SiFive Partners with Intel to Spark Innovation in High-Performance RISC-V Platforms – SiFive Press Release

RISC-V is Ready for Great Challenges – SiFive Blog

The Investment Heard Around The World – SiFive Blog

About SiFive:

As the pioneers who introduced RISC-V to the world, SiFive is transforming the future of compute by bringing the limitless potential of RISC-V to the highest performance and most data-intensive applications in the world. SiFive’s unrivaled compute platforms have enabled leading technology companies around the world to innovate, optimize and deliver the most advanced solutions of tomorrow across every market segment of chip design, including artificial intelligence, machine learning, automotive, data center, mobile, and consumer. With SiFive, the future of RISC-V has no limits. For more information, please visit SiFive.com.

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